

Jeffrey T. Oplinger

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OBJECTIVE To continue research or engineering endeavors in the fields of computer architecture and computer systems

EDUCATION Stanford University, Ph.D. candidate in Electrical Engineering
Degree expected September-October 2003

Stanford University, M.S. in Electrical Engineering
Graduated June 1995, 4.0 GPA

Carnegie Mellon University, B.S. in Computer Engineering
Graduated December 1993, 4.0 QPA

EXPERIENCE Stanford University, Stanford CA, 1995-2003
Graduate Research Assitant, Advisor: Monica S. Lam
Investigated the use of speculative threads for software reliability and developed a simulator to evaluate their performance; performed a limit study on potential speculative thread-level parallelism in integer programs

Intel Corporation, Portland OR, Jan-Aug 1993
Intern, Performance Validation team for the Pentium Pro

Microsoft Corporation, Redmond WA, Summer 1992
Intern, Software Design Engineering team for MS Excel

REFEREED PUBLICATIONS J. Oplinger and M. S. Lam, "Enhancing Software Reliability Using Speculative Threads," ASPLOS-X, October 2002.

J. Oplinger, D. Heine, and M. S. Lam, "In Search of Speculative Thread-Level Parallelism," PACT '99, October 1999.

SKILLS Languages: C, C++, Java, PERL, Verilog
Platforms: Unix (Mac OS X, Linux), Windows

HONORS NSF Research Fellowship
Stanford School of Engineering Fellowship
Andrew Carnegie Society Scholar

REFERENCES AVAILABLE UPON REQUEST